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U.S. **UTILITY** Patent Application

M.H O.I.P.E. SCANNED <u>UR</u> Q.A. <u>TC</u>	PATENT DATE
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/637214	F	740 75	302	2182	12/10

## APPLICANTS

Heinz Baier

**TITLE**

## Method and system for programming FPGAs on PC-cards without additional hardware

PTO-2040  
12/89[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
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